


Pages: 1

[Send to a friend](#) [Print](#)

cadence version sync (Read 835 times)

nxing
Junior Member
★★
Offline

God help the person who help themselves
Posts: 31
China

cadence version sync
Mar 27th, 2008, 5:18pm

Hi,

I am trying to study the version sync with Cadence, but can not find any documents either online or with Cadence. Can anybody give me a hint where I can find them?

Thanks,

nxing

[Back to top](#)

[Profile](#)

IP Logged

bernd
Senior Member
★★★★★
Offline


Re: cadence version sync
Reply #1 - Mar 31st, 2008, 3:51am

How about,

<install_dir>/doc/version sync/version sync.pdf

Bernd

Posts: 198
Munich/Germany

Just another lonesome cad guy

[Back to top](#)

[Profile](#) [WWW](#)

IP Logged

jbdavid
Community Fellow
★★★★★
Offline


Re: cadence version sync
Reply #2 - Apr 13th, 2008, 11:01pm

Its only saving grace is price.. and it only works in older versions of the CDB version of cadence tools. for just a little bit /seat you could get something like ICmanage or VersIC .. the nice thing about VersIC is that you can start out with Subversion (Open source) for the BackEnd of the revision system, then move to other supported (commercial) backends like perforce if you find the need.

But of course cadence tools haven't traditionally been built to take advantage of such systems.. (if it were, I'd be able to "diff" my schematic with the repository versions, even MERGE someone else's edit into my schematic.) and if that were possible - i might be able to merge the changes between 1033 and 1355 of That design, into THIS design!

chaos you say? but such features are considered _basic_ features in the world of open source code development.. (and I'll attest to their utility for script development too! (ocean/skill/perl/python etc))

someday, I hope..

Posts: 368
Silicon Valley

jbdavid
Mixed Signal Design Verification

[Back to top](#)

[Profile](#) [WWW](#)

IP Logged

Andrew Beckett
Senior Fellow
★★★★★
Offline


Re: cadence version sync
Reply #3 - Jan 3rd, 2009, 12:17am

Actually VersIC does now provide a means of diffing two designs or design versions. It's a rather nice feature, even if only used to tell you what changed between two revisions.

Life, don't talk to me about Life...

Doing a merge may be something that can be done in the future, but do remember that even merging when you have text files is often not as straightforward as it might seem. Not too bad if the merge is in different functions (although there may be interdependencies between those functions), but if merges are in the same function, there is much more likely to be problems - which is a similar problem to what there would be with a merge in a schematic.

Posts: 1190
Bracknell, UK

A schematic merge could potentially be done because you don't necessarily have to preserve the physical location of devices. Doing a merge on a layout would be much harder for anything other than trivial changes though.

Regards,

. . .